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Page No. 00CT18153314

**DIRECTOR,  
U.S. PATENT AND TRADEMARK OFFICE  
WASHINGTON, DC 20231**

Transmitted herewith for filing is the patent application of:

I HEREBY CERTIFY THIS PAPER OR FEE IS BEING DEPOSITED WITH THE U.S. POSTAL SERVICE "EXPRESS MAIL POST OFFICE TO ADDRESSEE" SERVICE UNDER 37 CFR 1.10 ON THE DATE INDICATED BELOW AND IS ADDRESSED TO: BOX PATENT APPLICATIONS, ASSISTANT COMMISSIONER FOR PATENTS, WASHINGTON, D.C. 20231.

**EXPRESS MAIL NO: EL747059590US**

**DATE OF DEPOSIT:** November 5, 2001

NAME: Jennifer Ferguson

SIGNATURE: Jennifer Tucker

11002 U. S. PTO

10/008586

11/05/01

**Inventors:** Russell FRANCIS  
Michele ALIA

**For: DEVICE AND METHOD FOR SELECTIVELY POWERING DOWN INTEGRATED CIRCUIT BLOCKS WITHIN A SYSTEM CHIP**

**Enclosed are:**

Patent Application: 19 pages, 11 claims is enclosed.  
5 sheets of drawings is enclosed.

The suggested drawing figure to be published is FIG. 4.

The suggested drawing figure to be published is FIG. 4.  
Citation Under 37 CFR 1.97 and PTO-1449 is enclosed.

A Preliminary Amendment is enclosed.

**Submission of Proposed Drawing Modification.**

Assignee info:

Name: STMicroelectronics S.r.l.

Address: Via C. Olivetti, 2, 20041 Agrate Brianza, Italy

Country of Incorporation: Italy

Applicant claims priority benefit to the following foreign application(s):

Country: Europe

Application No.: 00830732.4

Filing Date: November 3, 2000

The Declaration and Filing Fee are **NOT ENCLOSED**.

**[X]** Name, Address and Citizenship of Inventor(s) is as follows:

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☒ Associate this file with Customer No. 27975.



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Date: November 5, 2001

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